

# COURSE OUTLINE EE 342

Text: Sedra & Smith, Microelectronic Circuits, 5th edition

Section	Topic	Pages	Problems
<b>Chapter 2: Operational Amplifiers</b>			
2.1	Ideal OP AMP	63-68	2.2
2.2	Inverting configuration	68-77	2.8, D2.12, 2.16
2.3	Noninverting configuration	77-81	D2.44, D2.46, 2.49
2.4	Difference amplifiers	81-85	D2.71
2.6	Large signal operation	94-97	2.93, 2.95
2.7	DC imperfections	98-105	2.101, 2.103
2.8	Integrators	105-112	2.112
<b>Chapter 6 &amp; 7: Differential Amplifiers, IC biasing</b>			
7.3	The BJT differential pair	704-727	7.20,7.21,D7.24,7.38
7.4	Nonideal characteristics	720-723	7.51,
6.3	IC biasing – current mirrors	562-571	6.32, 6.33
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Apen.D	Single-time constant circuits	D1-D15	
Apen. E	Poles, Zeros, Bode plots	E1-E6	E.1, E.5, E.10
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4.9	Frequency response of the common-source amplifier	326-336	4.94, 4.99, 4.101
5.8	The BJT internal capacitance and high frequency model	485-490	
5.9	Frequency response of the common-emitter amplifier	491-503	5.159, 5.163, 5.167
6.4	High-frequency response general considerations	571-582	6.38, 6.46, 6.54
6.6	High-frequency response of the CS and CE amplifiers	588-600	6.66, 6.70
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